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03/06/02

PATENT NUMBER and  
ISSUE DATE

U.S. UTILITY Patent Application

APPL NUM 10090806	FILING DATE 03/06/2002	CLASS 450 257	SUBCLASS 55	GAU 2812 2826	BERE EXAMINER
<b>**APPLICANTS:</b> Kawase Masahiko; Kimoto Hidenobu; 2813					
<b>**CONTINUING DATA VERIFIED:</b> THIS APPLICATION IS A DIV OF 09/690,870 10/17/2000 US Pat No. 6,525,315 173 issued 2/25/03					
<b>** FOREIGN APPLICATIONS VERIFIED:</b> JAPAN 11-296085 10/19/1999 173					
PG-PUB <input type="checkbox"/> DO NOT PUBLISH <input type="checkbox"/>		RESCIND <input type="checkbox"/>			
Foreign priority claimed <input checked="" type="checkbox"/> yes <input type="checkbox"/> no		35 USC 119 conditions met <input checked="" type="checkbox"/> yes <input type="checkbox"/> no		ATTORNEY DOCKET NO 36856.648	
Verified and Acknowledged Examiners's initials					
TITLE : Chip-type composite electronic component and manufacturing method thereof U.S. DEPT. OF COMM./PAT. & TM-PTO-436L (Rev. 12-94)					

<b>NOTICE OF ALLOWANCE MAILED</b>		<b>CLAIMS ALLOWED</b>	
		Total Claims	Print Claim for O.G.
<b>ISSUE FEE</b>		<b>DRAWING</b>	
Amount Due	Date Paid	Sheets Drwg.	Figs. Drwg. Print Fig.
<input type="checkbox"/> <b>TERMINAL DISCLAIMER</b>		Application Examiner	
		<b>PREPARED FOR ISSUE</b>	
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